100

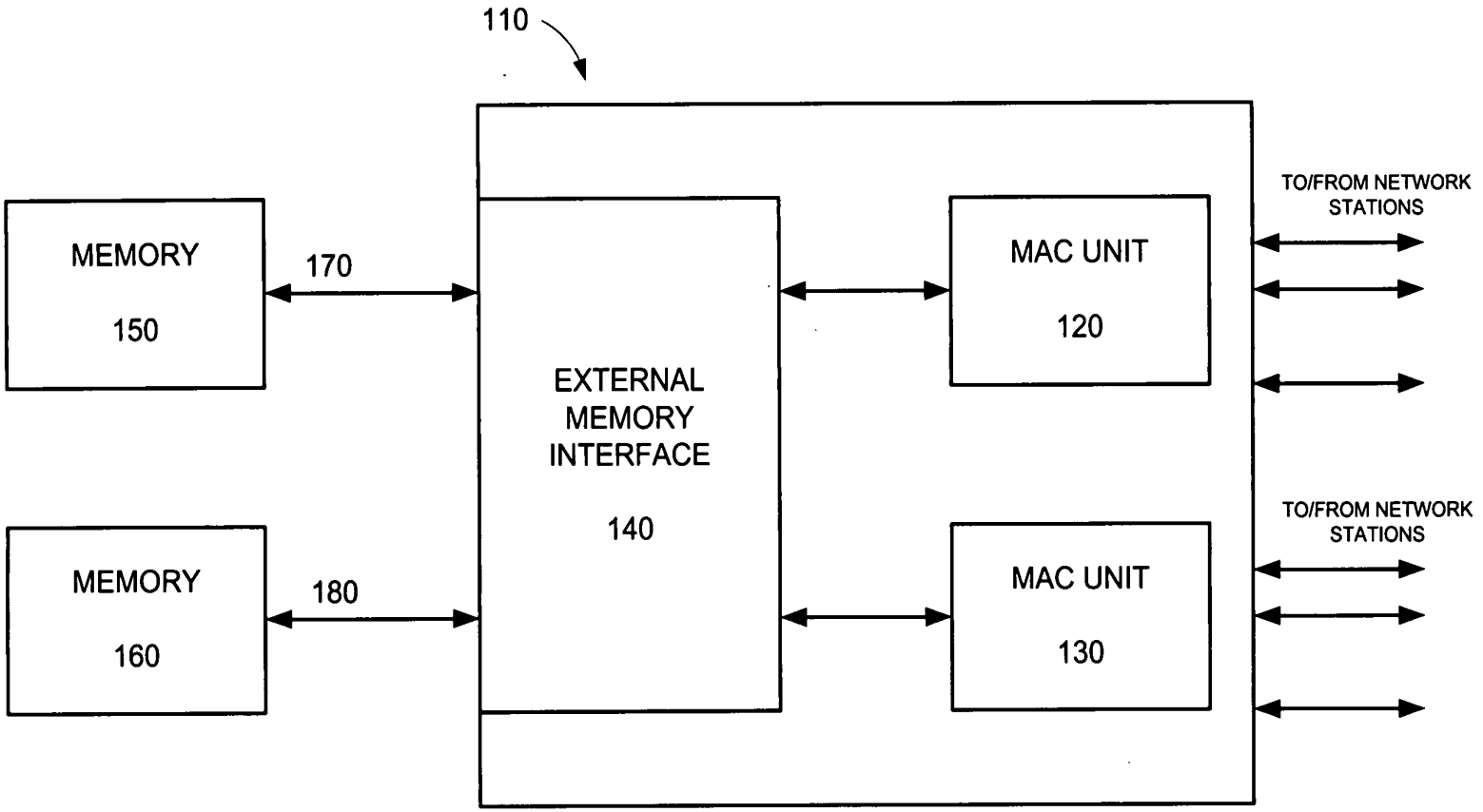
110
MEMORY
150
170
EXTERNAL
MEMORY
INTERFACE
140
MAC UNIT
120
TO/FROM NETWORK
STATIONS
MEMORY
160
180
MAC UNIT
130
TO/FROM NETWORK
STATIONS

FIG.1

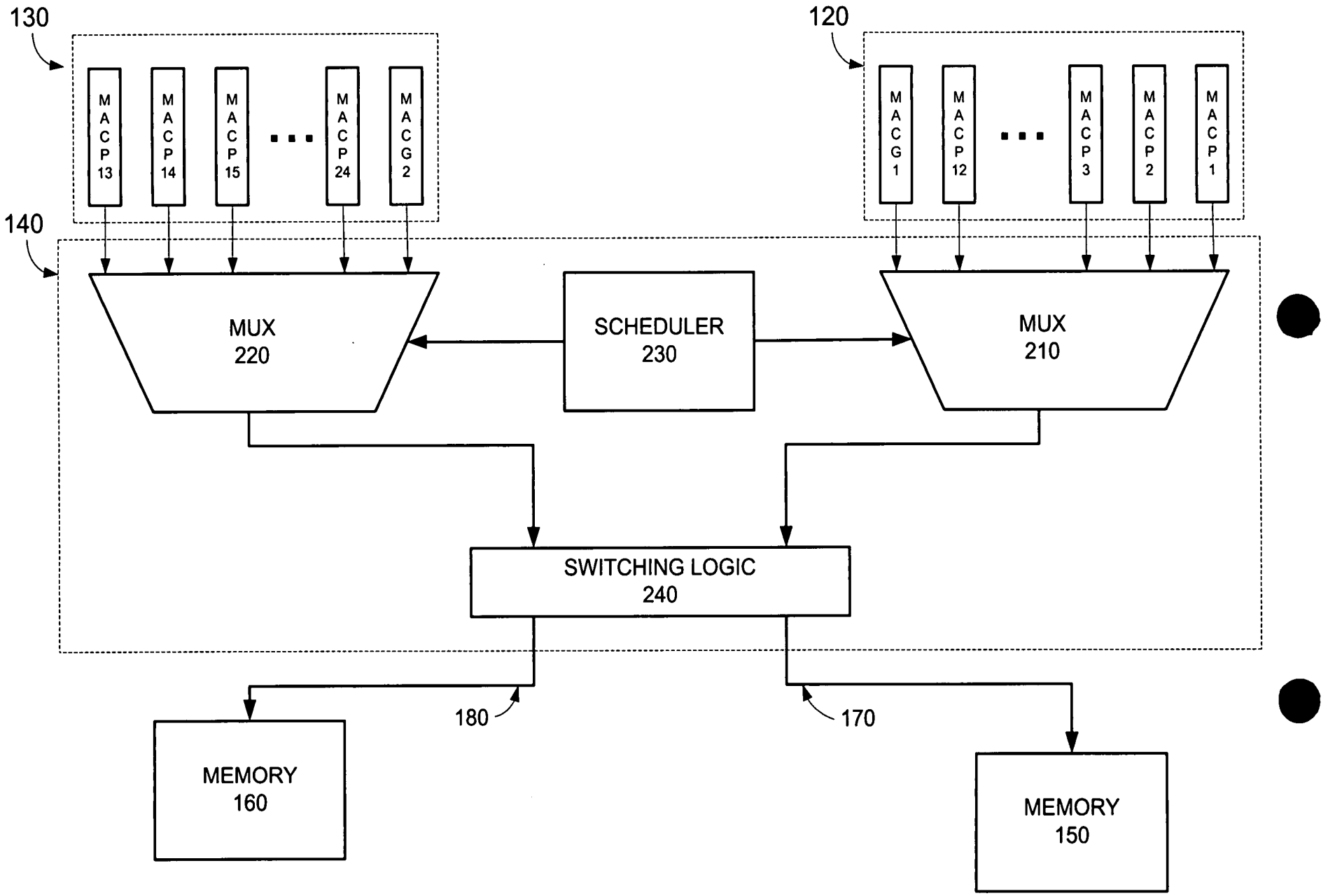
130
120
MACP 13
MACP 14
MACP 15
MACP 24
MACG 2
MACG 1
MACP 12
MACP 3
MACP 2
MACP 1
140
MUX 220
SCHEDULER 230
MUX 210
SWITCHING LOGIC 240
180
170
MEMORY 160
MEMORY 150

FIG. 2

09550450.090500

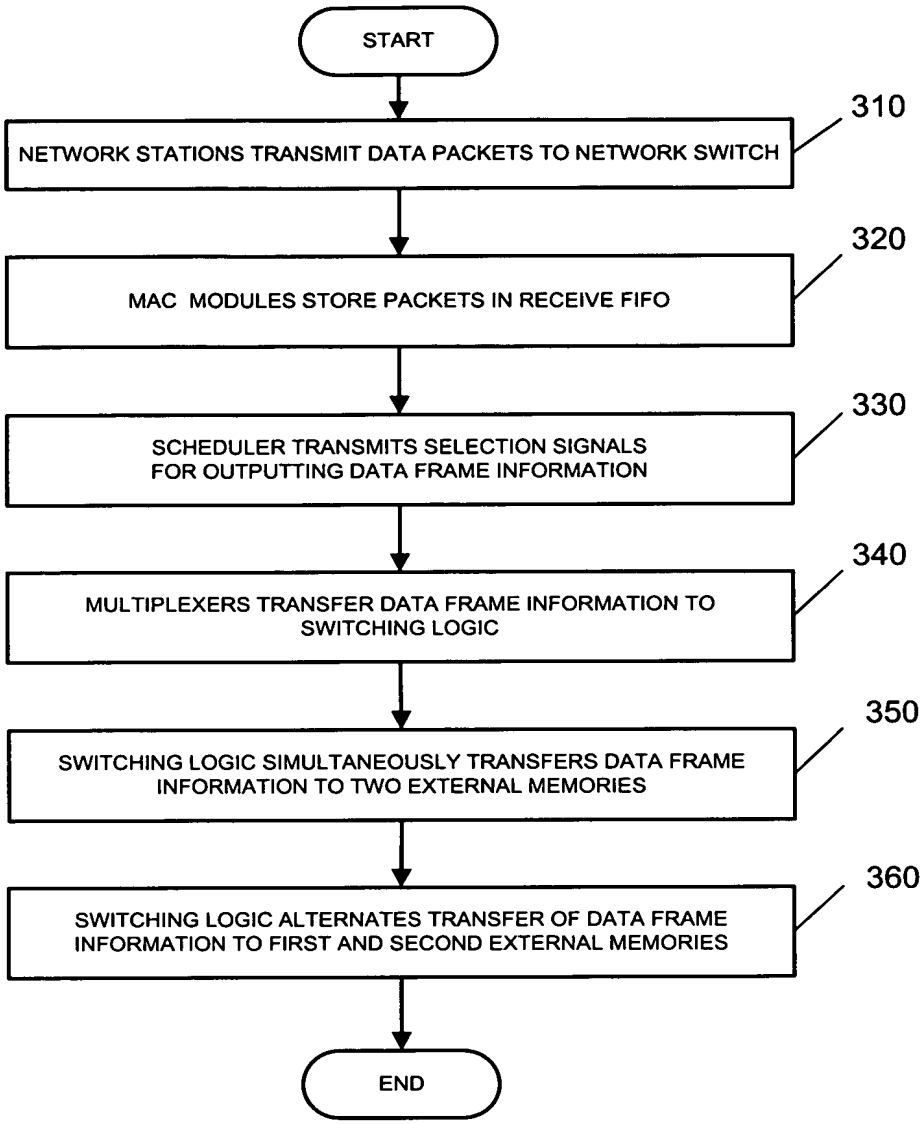

FIG. 3

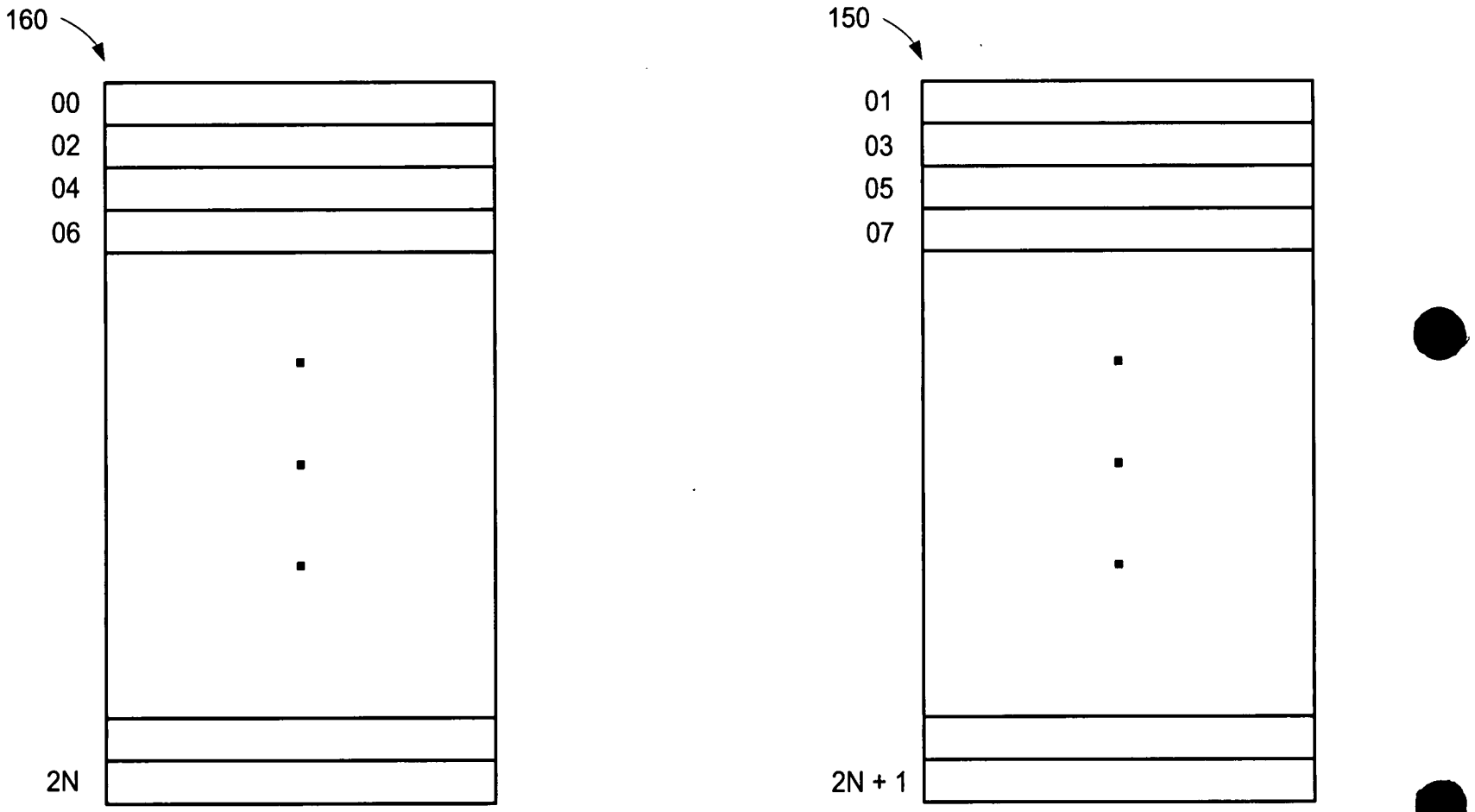

FIG. 4

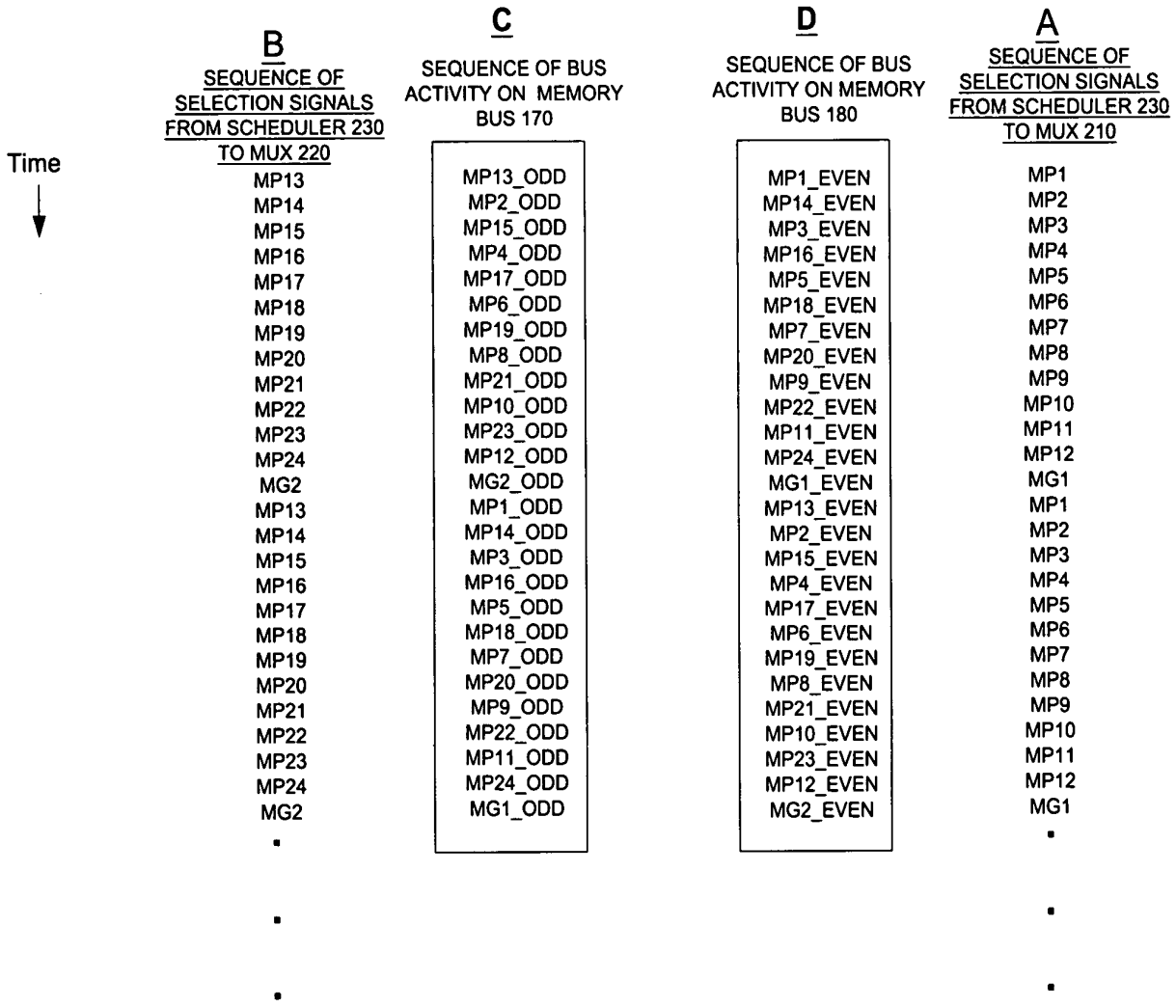
Time
B
SEQUENCE OF SELECTION SIGNALS FROM SCHEDULER 230 TO MUX 220
MP13
MP14
MP15
MP16
MP17
MP18
MP19
MP20
MP21
MP22
MP23
MP24
MG2
MP13
MP14
MP15
MP16
MP17
MP18
MP19
MP20
MP21
MP22
MP23
MP24
MG2
C
SEQUENCE OF BUS ACTIVITY ON MEMORY BUS 170
MP13_ODD
MP2_ODD
MP15_ODD
MP4_ODD
MP17_ODD
MP6_ODD
MP19_ODD
MP8_ODD
MP21_ODD
MP10_ODD
MP23_ODD
MP12_ODD
MG2_ODD
MP1_ODD
MP14_ODD
MP3_ODD
MP16_ODD
MP5_ODD
MP18_ODD
MP7_ODD
MP20_ODD
MP9_ODD
MP22_ODD
MP11_ODD
MP24_ODD
MG1_ODD
D
SEQUENCE OF BUS ACTIVITY ON MEMORY BUS 180
MP1_EVEN
MP14_EVEN
MP3_EVEN
MP16_EVEN
MP5_EVEN
MP18_EVEN
MP7_EVEN
MP20_EVEN
MP9_EVEN
MP22_EVEN
MP11_EVEN
MP24_EVEN
MG1_EVEN
MP13_EVEN
MP2_EVEN
MP15_EVEN
MP4_EVEN
MP17_EVEN
MP6_EVEN
MP19_EVEN
MP8_EVEN
MP21_EVEN
MP10_EVEN
MP23_EVEN
MP12_EVEN
MG2_EVEN
A
SEQUENCE OF SELECTION SIGNALS FROM SCHEDULER 230 TO MUX 210
MP1
MP2
MP3
MP4
MP5
MP6
MP7
MP8
MP9
MP10
MP11
MP12
MG1
MP1
MP2
MP3
MP4
MP5
MP6
MP7
MP8
MP9
MP10
MP11
MP12
MG1

FIG. 5